

ABSTRACT OF THE DISCLOSURE

A dual-display panel module has a shared ASIC chip. A primary-display panel module provides obverse-side image display, a secondary-display panel module provides reverse-side image display, a connector electrically connects the
5 primary-display panel and second-display panel, and a driver operatively is coupled to the primary display module and secondary display module, wherein the driver is supported in electrical connections the primary display module and the
10 secondary display modules via the connector.